

FIG. 1

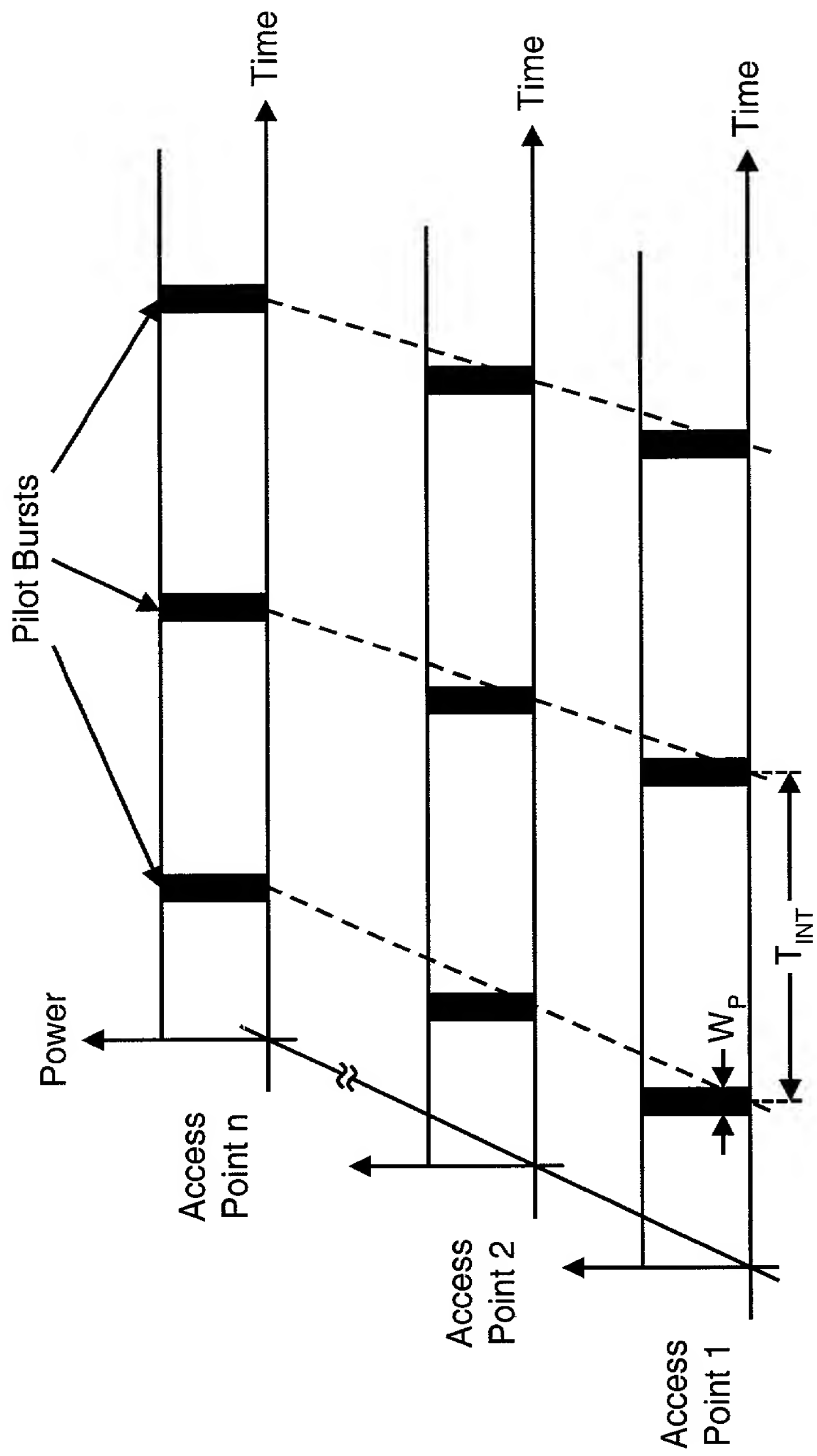
**FIG. 2**

FIG. 3 is a diagram illustrating a time slot structure for a communication system. The diagram shows a sequence of time slots, labeled n-3, n-2, n-1, n, n+1, n+2, and n+3, along a horizontal axis labeled "Time".

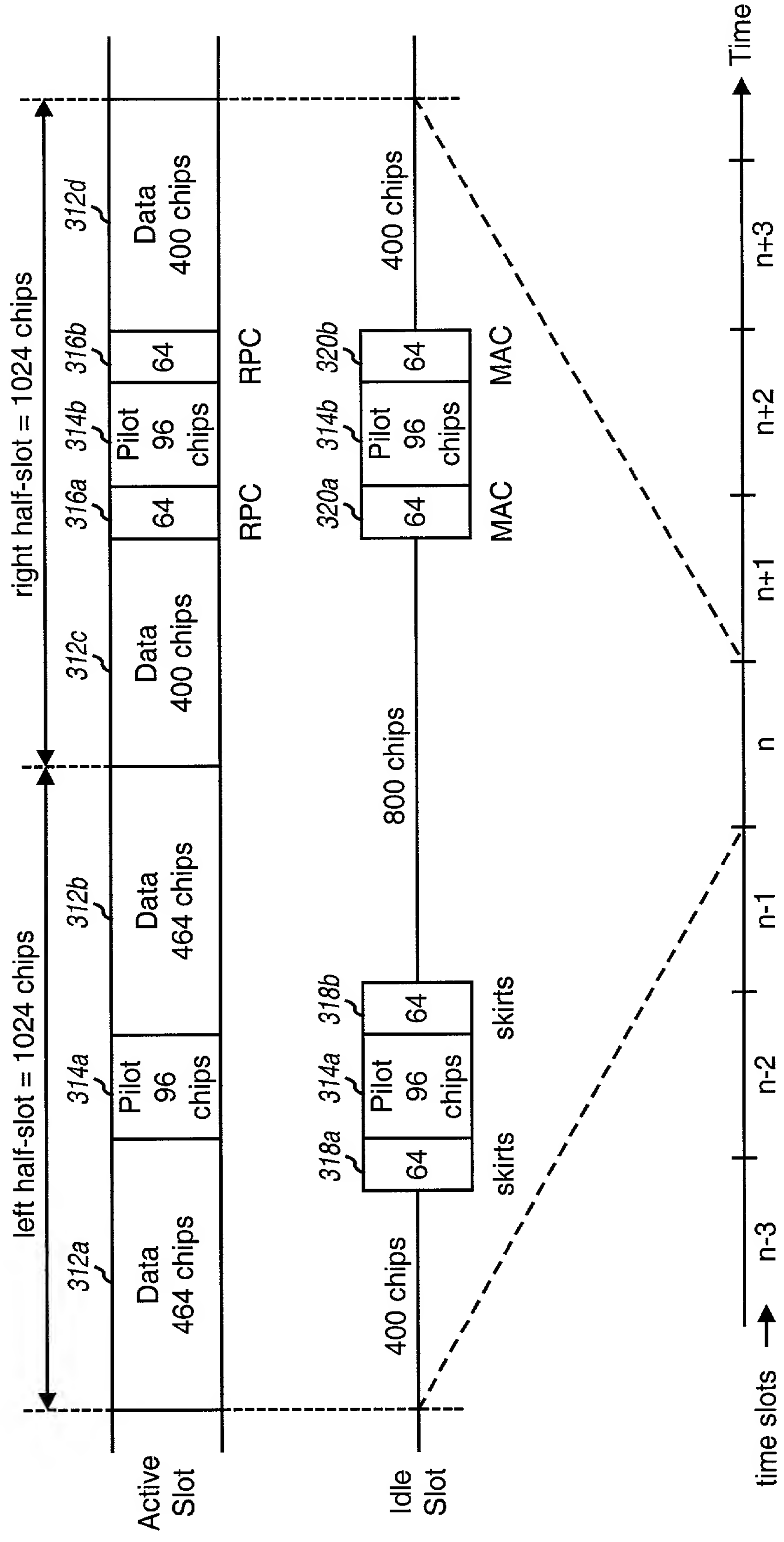
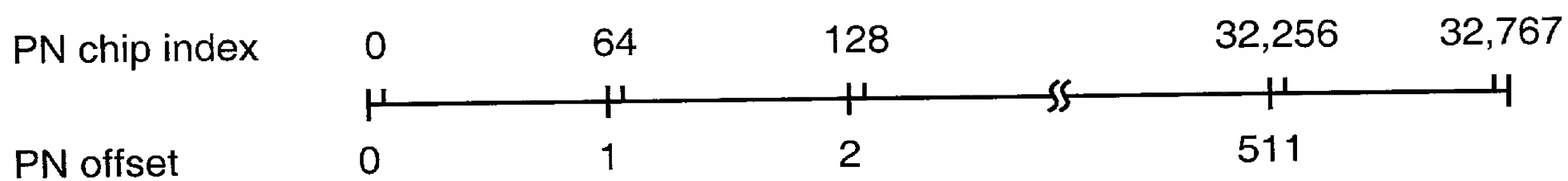
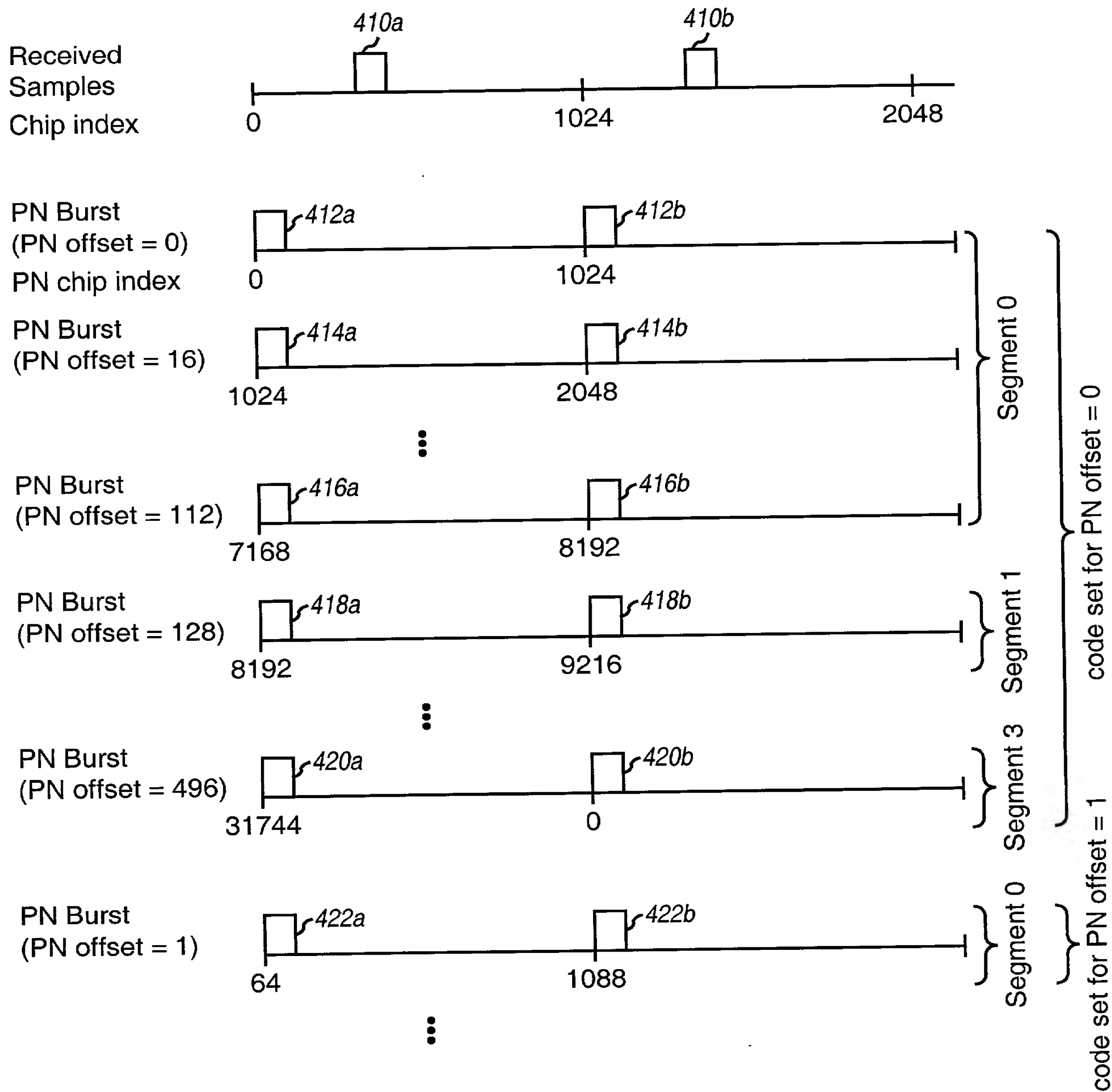


FIG. 3

**FIG. 4A****FIG. 4B**

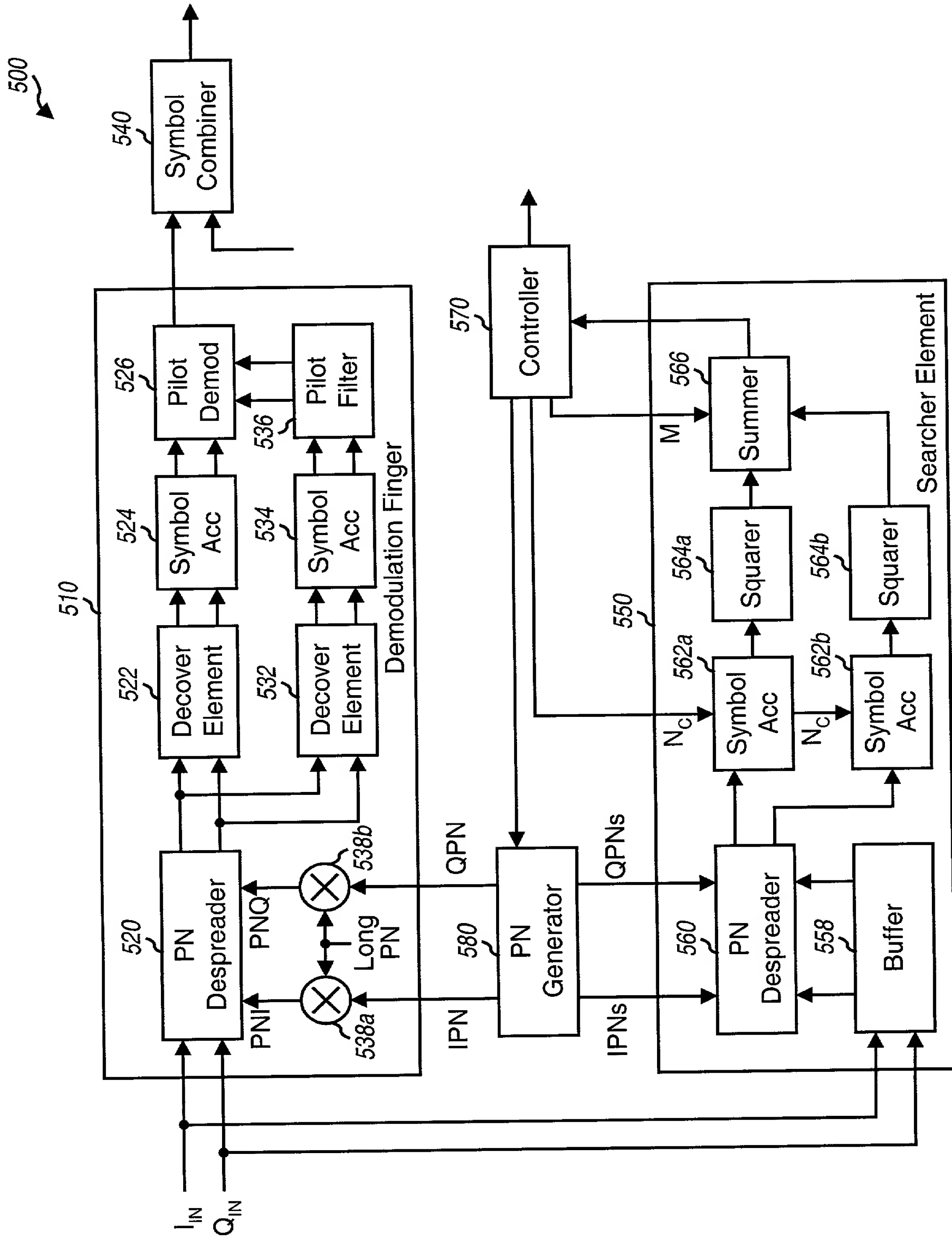


FIG. 5

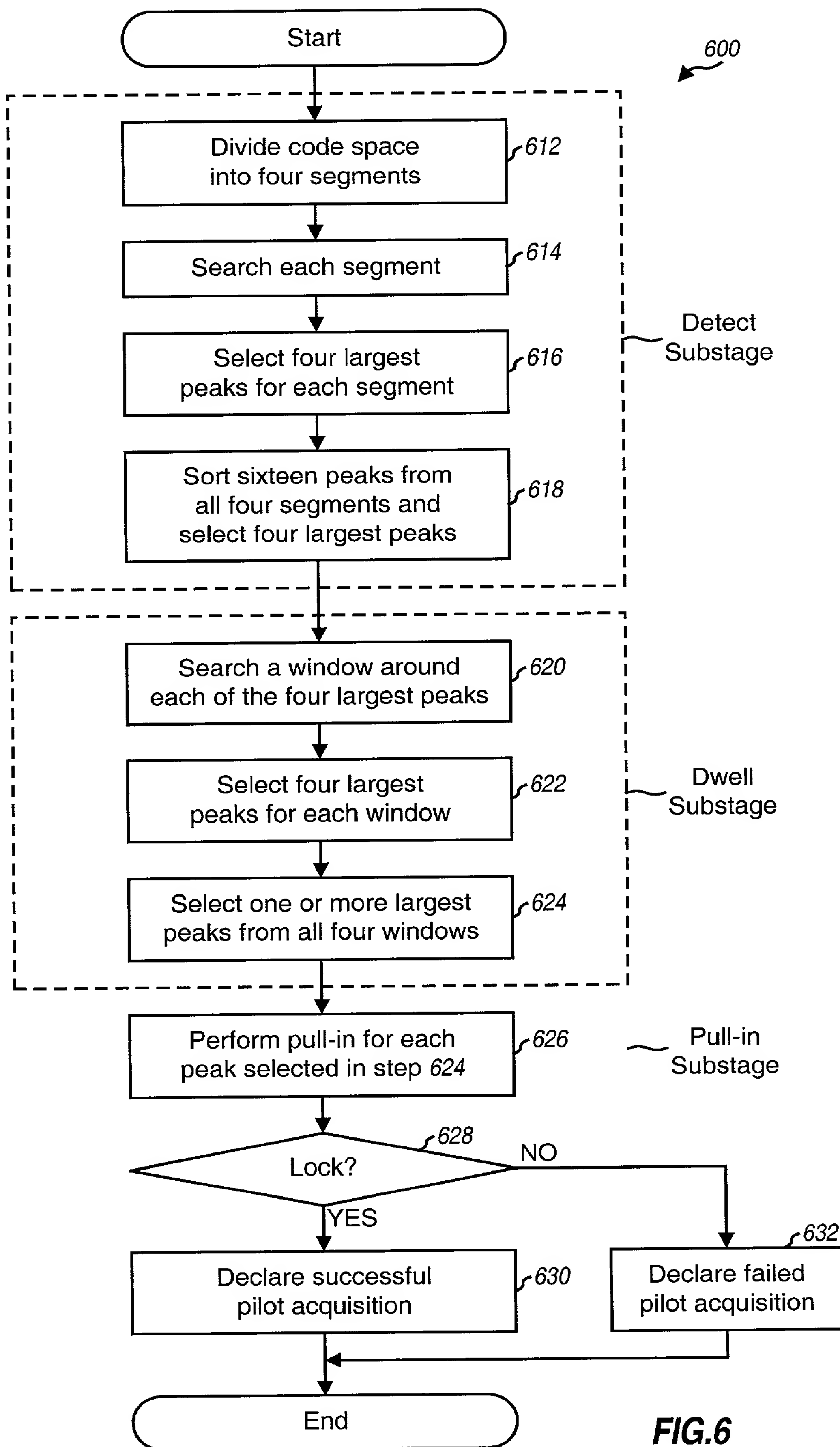


FIG.6

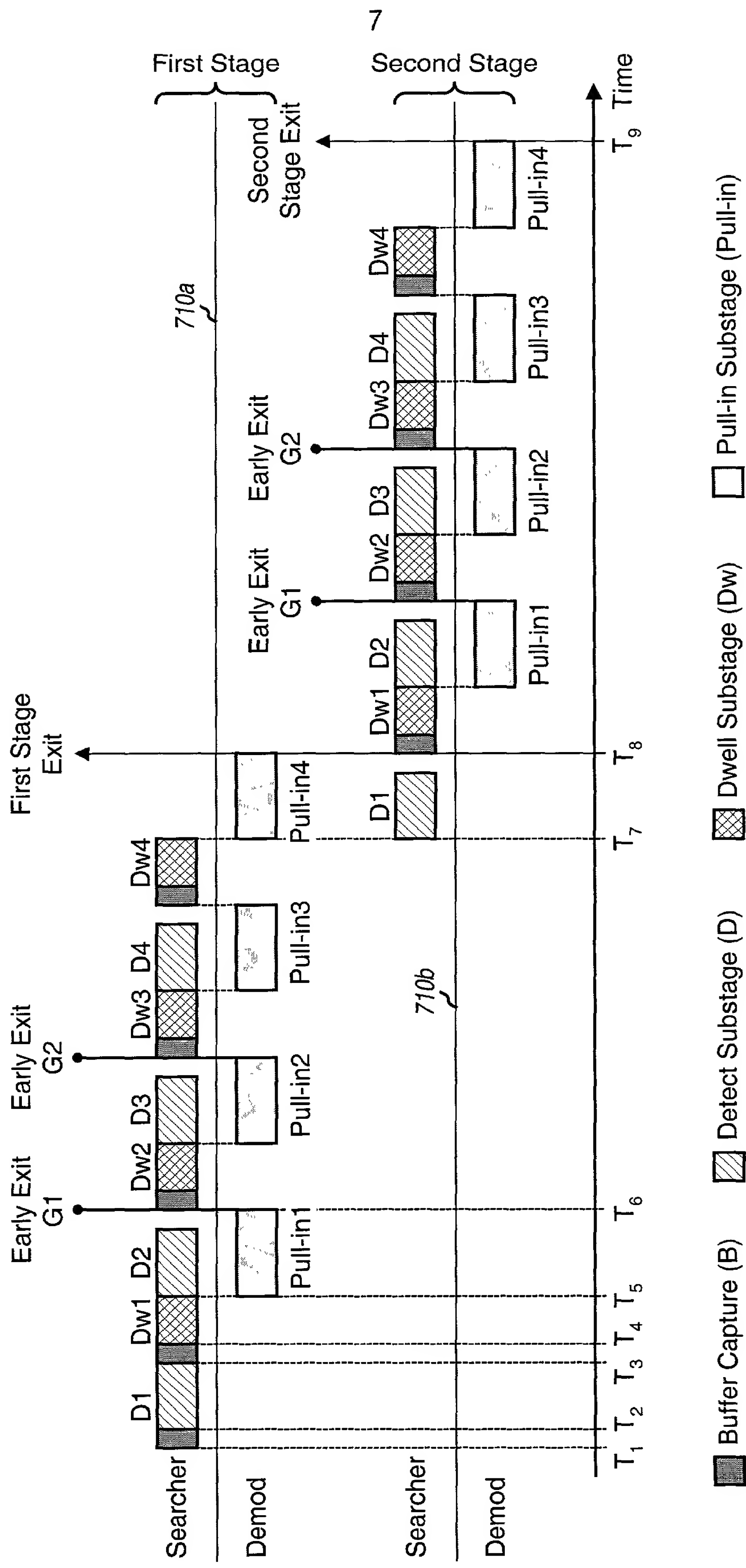


FIG. 7

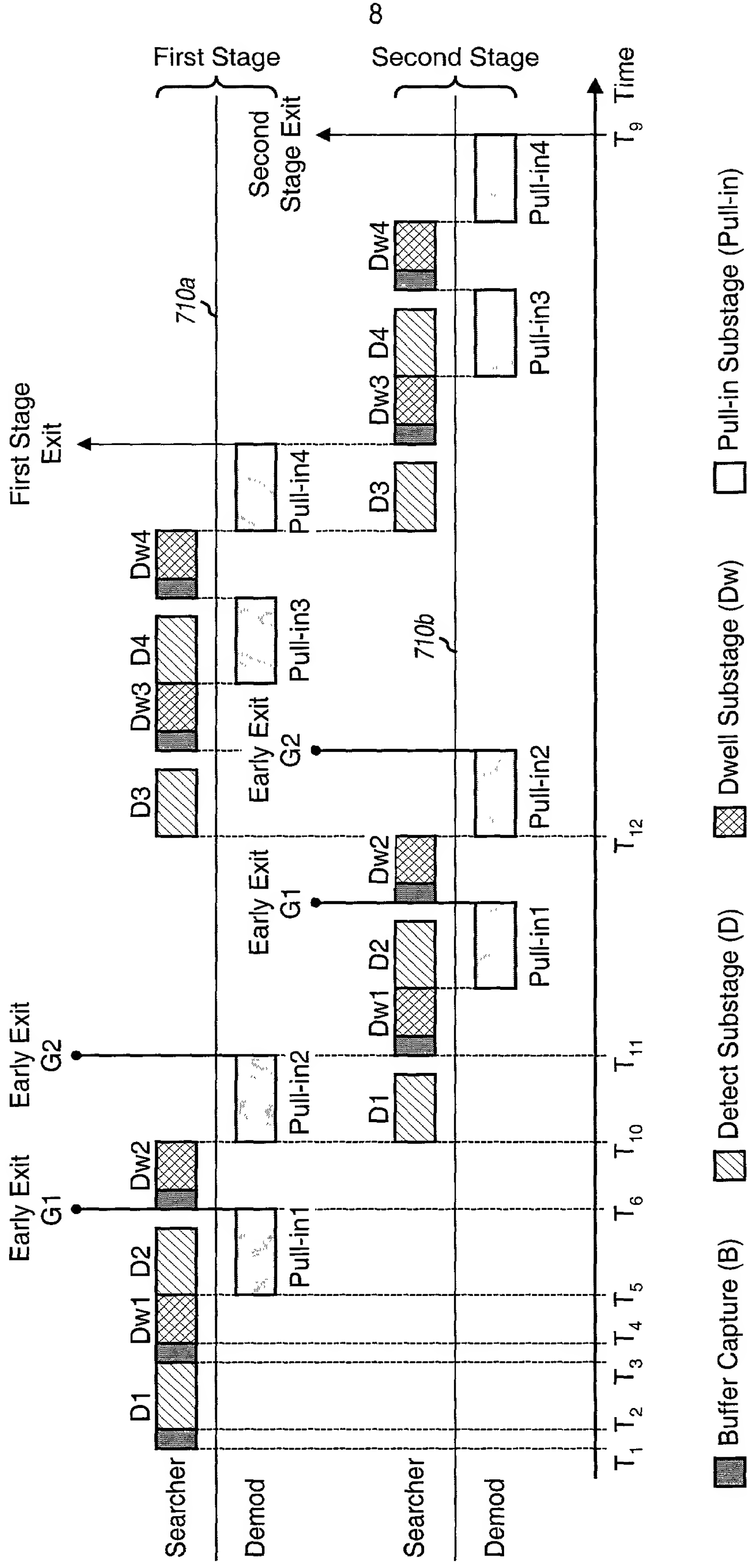


FIG. 8

